Rumore digitale (cenni)

**Figure 8.95.** 74AC244 octal buffer, driving seven 50pF loads $H \rightarrow L$, holding eighth output LOW. “Ground” is a copper plate (1 oz/ft$^2$).
(After Fig. 1.1-4, TI Advanced CMOS Logic Designer’s Handbook.)

**Figure 8.96.** It’s always a good idea to use robust low-inductance ground wiring, with liberal use of bypass capacitors.

**Figure 9.28.** Ground current noise.

$$I = C \frac{dV}{dt}$$

**Figure 9.29.** Capacitive-load ground current noise.

$$V = L \frac{dI}{dt}$$