

# Tabella delle porte logiche disponibili

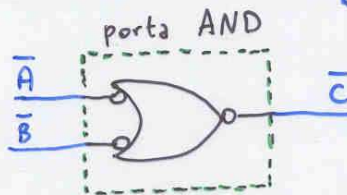
TABLE 01 - COMMON GATES IN THE 74 AND 74XX FAMILIES

Name	Expression	Symbol	Negative true symbol	Type	No. per chip	CMOS		Part number												
						4000B CMOS	74xx	ALS	AS	F	LS	C	AC(T)	HC(T)						
AND	$AB$			2-input	4	4081	7408													
				3-input	3	4073	7411													
				4-input	2	4082	7421													
NAND	$\overline{AB}$			2-input	4	4011	7400													
				3-input	3	4023	7410													
				4-input	2	4012	7420													
				8-input	1	4068	7430													
				13-input	1	—	74133													
OR	$A + B$			2-input	4	4071	7432													
				3-input	3	4075	—													
				4-input	2	4072	74802													
NOR	$\overline{A + B}$			2-input	4	4001	7402													
				3-input	3	4025	7427													
				4-input	2	4002	7425													
				5-input	2	—	74260													
				8-input	1	4078	—													
INVERT	$\overline{A}$				6	4069/4049	7404													
					8	—	74240													
BUFFER	$A$				6	4503/4050	74365													
					8	—	74241/244 (-541)-544)													
XOR	$A \oplus B$			2-input	4	4070	7486/386 (-135)													
XNOR	$\overline{A \oplus B}$			2-input	4	4077	74266 (-135)													
AOI				2-2-input	2	4085	7450/51													
				2-2-2-2-input	1	4086	7453/54													

es.: AND  $\left\{ \begin{array}{l} \text{TTL: } 74LS08 \\ \text{HIGH SPEED CMOS: } 74HC08 \end{array} \right.$   
 2 ingressi

Porta AND in logica negativa, cioè  $\left\{ \begin{array}{l} H = 0 \text{ logico} \\ L = 1 \text{ logico} \end{array} \right.$

A	B	$\overline{A+B}$
0	0	0
0	1	1
1	0	1
1	1	1



funzione equivalente = OR